

**Amendments to the Claims:**

This listing of claims will replace all prior versions, and listings, of claims in the above-identified application:

**Listing of Claims:**

1-39. (Cancelled).

40. (Currently Amended) A method of changing the output of a latch circuit, the output having a first state and a second state, the latch circuit comprising a SET transistor and a RESET transistor, the method comprising;

at the first state, conducting a first current in a first latch transistor and a SET transistor, said first latch transistor having a base coupled by a resistive element to the base of the SET transistor, and using a portion of the first current to bias the SET transistor at an operational point lower than a first threshold;

at the second state, conducting the first current in a second latch transistor and a RESET transistor, said second latch transistor having a base coupled by a resistive element to the base of the RESET transistor, and using a portion of the first current to bias the RESET transistor at an operational point lower than a second threshold;

to vary the output from the first state to the second state, providing a second current that raises the base of the SET transistor to the first threshold; and

to vary the output from the second state to the first state, providing the second current that raises the base of the RESET transistor to the second threshold.

41. (Previously presented) The method of claim 40, the providing the first current comprising providing the first current from two substantially equal current sources.

42. (Previously presented) The method of claim 41, the providing the second current comprising causing a relatively small voltage change in the base of one of the SET and RESET transistors.

43. (Previously presented) An oscillator that oscillates an output using the method of claim 40.

44. (Previously presented) The oscillator of claim 43 further comprising temperature compensating the oscillator.